

## **BIPOLAR JUNCTION TRANSISTOR AND FABRICATING METHOD**

Appl. No.	:	10/709,568	Confirmation No.	3567
Applicant	:	Anchor Chen		
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TC/A.U.	:	2818		
Examiner	:	Luu, Chuong A		
Docket No.	:	NAUP0486USA4		
Customer No.	:	27765		

Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

### **AMENDMENT**

- 5 Please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Drawings** begin on page 3 of this paper and include an attached replacement sheet.

- 10 **Remarks** begin on page 4 of this paper.

An **Appendix** including amended drawing figure is attached following page 4 of this paper.